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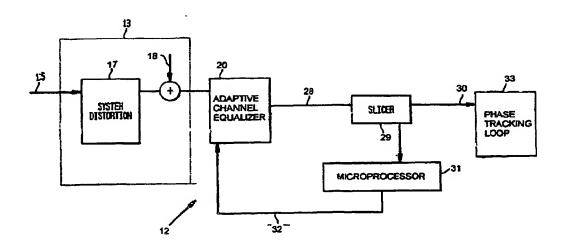
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(54) Title: EQUALIZER STATUS MONITOR '



(57) Abstract: A system for monitoring the output (28) of an adaptive channel equalizer (20) in order to determine if convergence has been achieved. A slicer (29) samples data from the equalizer (20) during a predetermined period. The output data (30) from the slicer (29) is forwarded to a microprocessor (31) in order to apply a test standard to the slicer data. For example, if one of every possible transmitted symbol is detected by the microprocessor (31), convergence is assumed to have occurred. If the test criterion is not met, a reset signal (32) is sent to the equalizer (20).

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International application No.

PCT/US03/11002

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) Please See Continuation Sheet		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
propriate, of the relevant passages Relevant to claim No.		
figure 8 1-3, 11, 12, 18, 19, 21 and 23 1-23		
See patent family annex.		
"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention  "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone		
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